

Revisions:

- Changed I/O solder pad area to 0.3" and 0.4" through hole footprints
- Moved I/O pads .1" away from card edge
- Added solder pad option to connect A0 to RSMC circuit
- Increased board footprint to 3.90"
- Tied RE/ to GND
- Moved layout over .200" for DIN clearance

Notes:

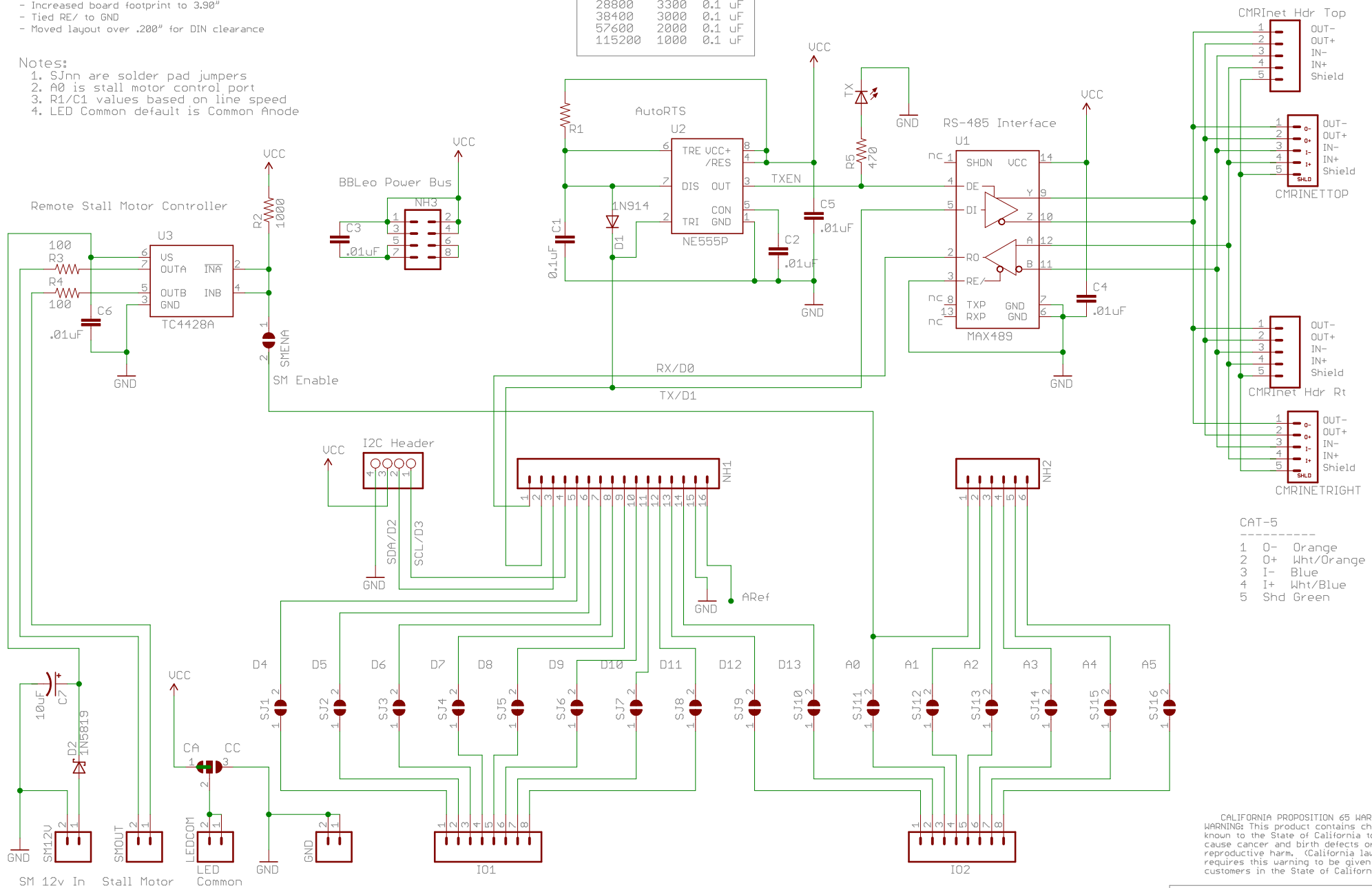
1. SJnn are solder pad jumpers
2. A0 is stall motor control port
3. R1/C1 values based on line speed
4. LED Common default is Common Anode

AutoRTS Values

BPS	R1	C1
9600	12000	0.1 uF
19200	12000	0.1 uF
28800	3300	0.1 uF
38400	3000	0.1 uF
57600	2000	0.1 uF
115200	1000	0.1 uF

Note:

9600/19200 is default
AutoRTS will operate across the speed range with 0.1uF and 12k



Released under the Creative Commons Attribution Share-Alike 3.0 License
<http://creativecommons.org/licenses/by-sa/3.0>
 Design by: Model Railroad Control Systems

cpNode Control Point	
Title: CPNODE	Designer: Chuck Catania
Date: 04/21/2016	Rev: v2.0